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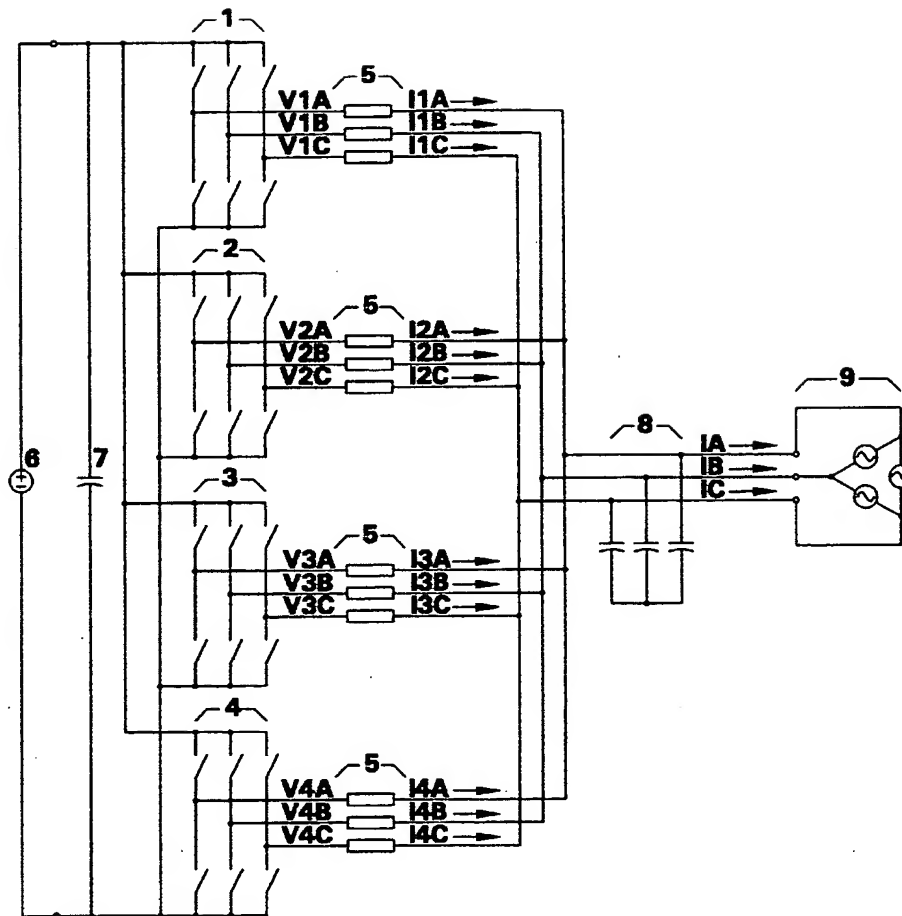
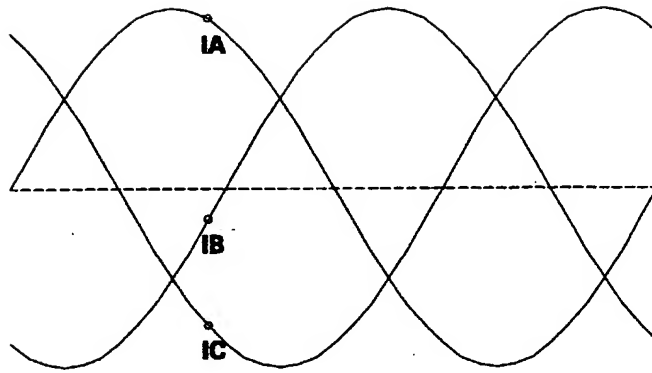


Fig. 1



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V1A

V2A

V3A

V4A

V1B

V2B

V3B

V4B

V1C

V2C

V3C

V4C

I1A

I2A

I3A

I4A

I1B

I2B

I3B

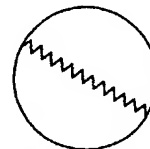
I4B

I1C

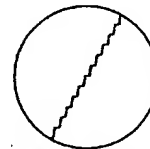
I2C

I3C

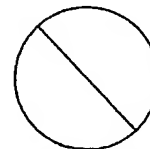
I4C



IA DETAIL



IB DETAIL



IC DETAIL

Fig. 2



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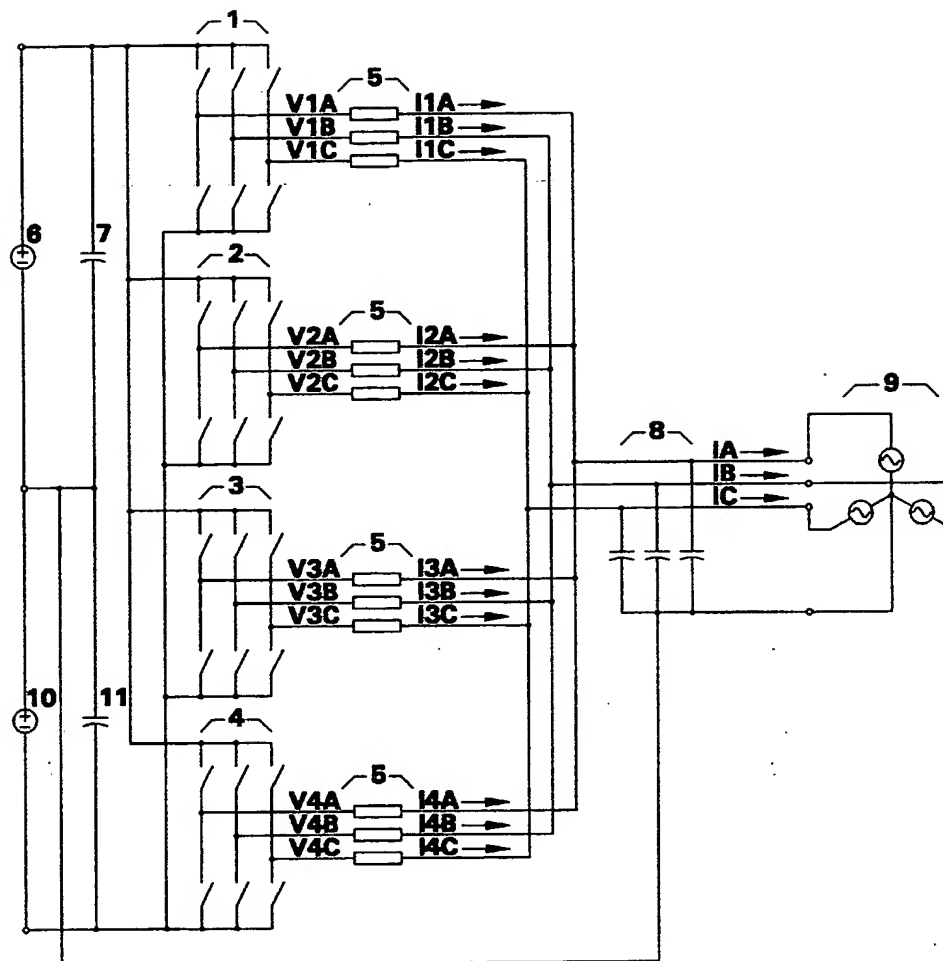


Fig. 3

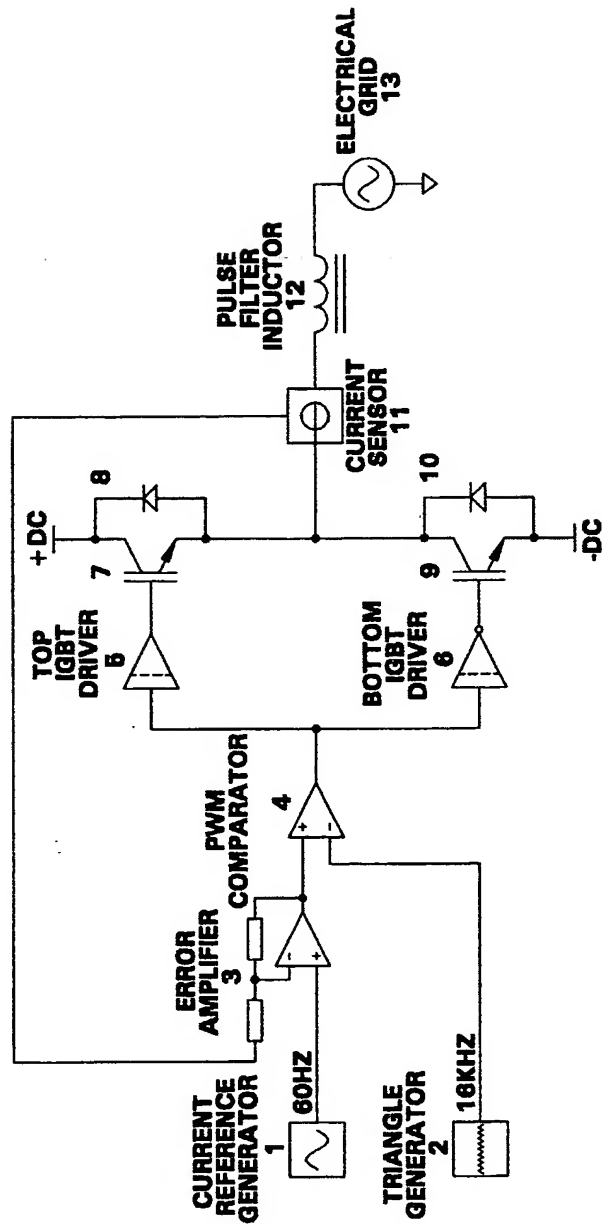


Fig. 4